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CONFIRMATION NO. 3348

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UPDATED FILING RECEIPT



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Applicant(s)

Amit Ramchandran, San Jose, CA;

Assignment For Published Patent Application

QuickSilver Technology, Inc., San Jose, CA;

Domestic Priority data as claimed by applicant

This appln claims benefit of 60/422,063 10/28/2002

Foreign Applications

If Required, Foreign Filing License Granted: 10/23/2003

Projected Publication Date: 05/13/2004

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

Cache for instruction set architecture

Preliminary Class

711

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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